

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date
28 July 2005 (28.07.2005)

PCT

(10) International Publication Number
WO 2005/069601 A1

(51) International Patent Classification⁷: **H04N 3/15, 5/32**

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(21) International Application Number:

PCT/IB2005/050092

(72) Inventors; and

(22) International Filing Date: 7 January 2005 (07.01.2005)

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(25) Filing Language: English

English

(26) Publication Language: English

English

(30) Priority Data:
04100073.8 12 January 2004 (12.01.2004) EP

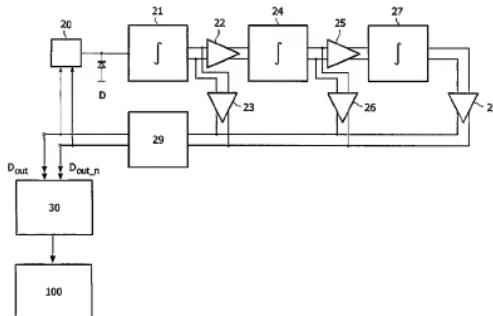
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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,

[Continued on next page]

(54) Title: SEMICONDUCTOR-BASED IMAGE SENSOR



(57) Abstract: A detector arrangement and/or a semiconductor-based image sensor with a plurality of detector elements or image pixels is described, which each have an integrated SD- (Sigma Delta) Modulator (20 to 29) or an integrated SD-A/D-(Sigma Delta Analog/Digital) converter (20 to 30), as well as particularly such a detector arrangement and/or such an image sensor on the basis of a CMOS-semiconductor. Particularly on the basis of the differential version and/or the multi-phase structure of the SD modulator and the SD-A/D converter, a detector arrangement and/or an image sensor with specially high noise robustness, a high dynamic range and a lesser noise can be produced, so that this is particularly suitable for usage in Computer Tomography (CT) apparatus.

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CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SI, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TI, TM),

European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

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